

D1  
Cont'd.

wherein said shared memory is adapted to copy contents of one of said multi-port memories, which has been changed by a writing operation from said user side, to other multi-port memories through said at least one copybus.

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Please add new dependent Claim 6 as follows:

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D2

6. ~~The shared memory according to Claim 1, wherein each memory has a plurality of ports wherein each port of said plurality of ports has a concurrent-access function that allows for internally concurrent access via the port to data stored in the memory.~~

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#### REMARKS

This Preliminary Amendment is a supplemental amendment to the Preliminary Amendment filed on November 12, 2002 along with the Continued Prosecution Application. This Supplemental Preliminary Amendment further amends Claim 1 of the application and adds a new dependent Claim 6. Applicant provides remarks below concerning these amendments, which address various issues that were raised during prior prosecution of the application. Applicant respectfully requests entry of the amendments in this Supplemental Preliminary Amendment and the amendments in the Preliminary Amendment filed November 12, 2002. Applicant request that the Examiner discard the remarks in the Preliminary Amendment filed November 12, 2002 in favor of the remarks provided below.

In previous prosecution of the application, the claims have been rejected as either anticipated by U.S. Patent No. 6,321,298 to Hubis or obvious in light of the Hubis '298 patent in combination with either U.S. Patent No. 6,223,260 to Gujral et al. or U.S. Patent No. 6,021,472 to Hamaguchi.

With regard to Figure 3 of the Hubis '298 patent, a previous Office Action has alleged that the controllers 104 and the memories 202 located therein constitute shared memories. It is alleged that bus 110 acts as a copy bus, and because each controller 104 has an independent connection to the host 102, the arrangement of the Hubis '298 patent is a shared memory anticipating the claimed invention.